

2025/10/05 ver1, 10/06 ver2,

Non Inverted Configuration

The diagram illustrates a non-inverted configuration for an AD797 op-amp. The circuit includes two op-amp buffers. The first buffer (AD797) has its non-inverting input connected to a remote connector on the PCB through a 10k resistor and a jumper pin. Its output is connected to a remote connector on the PCB through a 10k resistor and a jumper pin. The second buffer (AD797) has its non-inverting input connected to a remote connector on the PCB through a 10k resistor and a jumper pin. Its output is connected to a remote connector on the PCB through a 10k resistor and a jumper pin. The circuit also includes a 50 ohm terminator and an oscilloscope input.

According to test circuit, 2.2V was recorded at 100kHz frequency.

The top diagram shows a TA7815S positive voltage regulator. Its input is connected to a +20V source through a resistor labeled '0'. The output is connected to a +15V source. The regulator's ground pin is connected to ground. Two bypass capacitors are shown: a 100uF capacitor between the input and ground, and a 1uF capacitor between the output and ground.

The bottom diagram shows a TA79015S negative voltage regulator. Its input is connected to a -20V source through a resistor labeled '0'. The output is connected to a -15V source. The regulator's ground pin is connected to ground. Two bypass capacitors are shown: a 100uF capacitor between the input and ground, and a 1uF capacitor between the output and ground.